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erver/Workstation Motherboard

# SPC741D8UD-2T SPC741D8UD-2T/X550

User Manual



Version 1.10 Published February 2024

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- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following

#### measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

# $C \in$

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DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.

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# **Chapter 1 Introduction**

Thank you for purchasing ASRock Rack *SPC741D8UD-2T / SPC741D8UD-2T/X550* motherboard, a reliable motherboard produced under ASRock Rack's consistently stringent quality control. It delivers excellent performance with robust design conforming to ASRock Rack's commitment to quality and endurance.

In this manual, chapter 1 and 2 contains introduction of the motherboard and step-by-step guide to the hardware installation. Chapter 3 and 4 contains the configuration guide to BIOS setup and information of the Support Software.



Because the motherboard specifications and the BIOS software might be updated, the content of this manual will be subject to change without notice. In case any modifications of this manual occur, the updated version will be available on ASRock Rack website without further notice. You may find the latest memory and CPU support lists on ASRock Rack website as well. ASRock Rack's Website: www.ASRockRack.com

If you require technical support related to this motherboard, please visit our website for specific information about the model you are using. http://www.asrockrack.com/support/

# 1.1 Package Contents

- ASRock Rack SPC741D8UD-2T/SPC741D8UD-2T/X550 Motherboard (Deep Micro-ATX Form Factor: 10.4-in x 10.5-in)
- · Quick Installation Guide
- 1 x I/O Shield
- 1 x Mini SAS HD to 4\*SATA Cable (60cm)
- 1 x ATX 4P to 24P Power Cable (8cm)
- · 1 x Screw for M.2 Socket
- 2 x CPU Non-Fabric Carriers (1xE1A, 1xE1B)



 $If any items \ are \ missing \ or \ appear \ damaged, \ contact \ your \ authorized \ dealer.$ 

# 1.2 Specifications

SPC741D8UD-2T / :	SPC741D8UD-2T/X550
Physical Status	
Form Factor	Deep Micro-ATX
Dimension	10.4" x 10.5" (264.16 x 266.7 mm)
Processor System	
CPU	Supports 5 <sup>th</sup> and 4 <sup>th</sup> Gen Intel® Xeon® Scalable processors
Socket	Single Socket E (LGA 4677)
Thermal Design	350W
Power (TDP)	
Chipset	Intel® C741
System Memory	
Supported DIMM	8 DIMM slots (1DPC)
Quantity	
Supported Type	288-pin DDR5 RDIMM/RDIMM-3DS
Max. Capacity per	RDIMM:96GB
DIMM	RDIMM-3DS: 2H- 128 GB / 4H- 256 GB
Max. DIMM	5600 MT/s on 5th Gen Intel® Xeon® Scalable Processors
Frequency	4800 MT/s on 4th Gen Intel® Xeon® Scalable Processors
Voltage	1.1V
Note: Memory support is	to be validated
PCIe Expansion Slot	ts (SLOT7 close to CPU)
SLOT7	PCIe5.0 / CXL1.1 x16 [CPU]
SLOT6	PCIe5.0 / CXL1.1 x16 [CPU]
SLOT5	PCIe5.0 / CXL1.1 x16 [CPU]
SLOT4	PCIe5.0 / CXL1.1 x16 [CPU]
Other PCIe Expansi	on Connectors
M.2	1 M-key (PCIe3.0 x4 or SATA 6Gb/s); support 2280 form factor
	[PCH]
MCIO	2 MCIO (PCIE5.0 x8) [CPU]
SATA/SAS Storage	
PCH Built-in	Intel® C741 (9 SATA 6Gb/s, support RAID 0/1/5/10):
Storage	1 Mini-SAS HD 8i for 8 SATA, 1 M.2
Ethernet	
Additional GbE	SPC741D8UD-2T:
Controller	2 RJ45 (10GbE) by Intel® X710-AT2
	SPC741D8UD-2T/X550:
	2 RJ45 (10GbE) by Intel® X550-AT2
Graphics	
Controller	ASPEED AST2600:
	1 DB15 (VGA)
	1

Rear I/O	
UID Button/LED	1 UID button w/ LED
VGA Output	1 DB15 (VGA)
USB	2 Type A (USB3.2 Gen1)
RJ45	2 RJ45 (10GbE), 1 dedicated IPMI
Hardware Monitor	2 IO 15 (10 Gbb), I dedicated II MI
Temperature	CPU, PCH, MB, Card side Temperature Sensing
Fan	Fan Tachometer
1 all	
	CPU Quiet Fan (Allow Chassis Fan Speed Auto-Adjust by
	CPU Temperature)
	Fan Multi-Speed Control
Voltage	1.05V_PCH, 1.8V_PCH, +BAT, PVNN_PCH, 3.3V, 5V, 12V,
	3.3VSB, 5VSB, 12V, +12VSB
Server Management	
BMC Controller	ASPEED AST2600: IPMI2.0 with iKVM and vMedia support
IPMI Dedicated	1 RJ45 Dedicated IPMI LAN port by Realtek RTL8211F
GLAN	
System BIOS	
Туре	AMI UEFI BIOS; 512Mb SPI Flash ROM
Features	Plug and Play (PnP), ACPI 4.0 and above compliance wake up
	events, SMBIOS 3.4 and above, ASRock Rack Instant Flash
Internal Connectors	/Headers
PSU Connector	1 Mirco-Fit (4-pin, ATX PSU signal) w/ATX 24-pin adapter
	cable, 3 (8-pin, ATX 12V) support 12V DC-in
Other Power	1 Micro-Fit (6-pin) for HDD power when using 12V DC-in
Connectors	power source
Auxiliary Panel	1 (18-pin): chassis intrusion, system fault LED, LAN1/LAN2
Header	activity LED, locate, SMBus
System Panel	1 (9-pin): power switch, reset switch, system power LED, HDD
	activity LED
NMI Header	1
COM Header	1 (9-pin)
Speaker Header	1 (4-pin)
Fan Header	6 (6-pin)
Thermal Sensor	1
Header	
TPM Header	1 (13-pin, SPI)
VROC header	1
HSBP	1
PMbus Header	1
SMbus Header	SPC741D8UD-2T: 1
	SPC741D8UD-2T/X550: 2
USB 2.0 Header	SPC741D8UD-2T: N/A
	SPC741D8UD-2T/X550: 1 (9-pin, 2 USB 2.0)
	or or 112002 21/1030. 1 (2 pm, 2 002 2.0)

IPMB Header	1		
Clear CMOS	1 (contact pads)		
LED Indicators			
Standby Power	1 (5VSB)		
LED			
80 Debug Port LED	1		
Fan Fail LED	6		
BMC Heartbeat	1		
LED			
Supported OS			
OS	Microsoft® Windows®:		
	- Server 2022 (64 bit)		
	Linux*:		
	- Red Hat Enterprise Linux 8.4 LTS, 8.5		
	- SUSE Enterprise Linux SLE 15 SP3		
	- Ubuntu 21.10, 22.04 LTS		
	Hypervisor:		
	- VMWare® ESXi		
	*Please refer to the website for the latest OS support list.		
Environment			
Temperature	Operation temperature: 10°C ~ 35°C (50 - 95 degF)/ Non		
	operation temperature: -40°C ~ 70°C (-40 - 158degF)		
Humidity	Non operation humidity: 20% ~ 90% ( Non condensing)		

NOTE: Please refer to the website for the latest specifications.

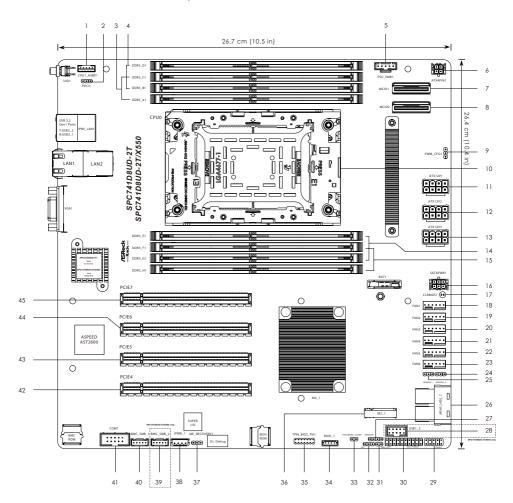


If you install Intel\* LAN utility or Marvell SATA utility, this motherboard may fail Windows\* Hardware Quality Lab (WHQL) certification tests. If you install the drivers only, it will pass the WHQL tests.

# 1.3 Unique Features

ASRock Rack Instant Flash is a BIOS flash utility embedded in Flash ROM. This convenient BIOS update tool allows you to update system BIOS without entering operating systems first like MS-DOS or Windows. With this utility, you can press the <F6> key during the POST or the <F2> key to enter into the BIOS setup menu to access ASRock Rack Instant Flash. Just launch this tool and save the new BIOS file to your USB flash drive, floppy disk or hard drive, then you can update your BIOS only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system.

# 1.4 Motherboard Layout

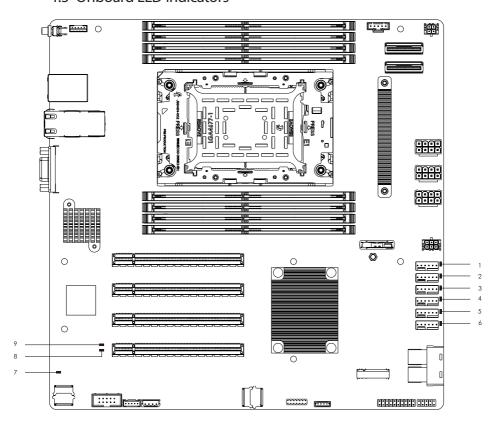


No.	Description
1	Backplane PCI Express Hot-Plug Connector (CPU1_HSBP1)
2	CPU PECI Mode Jumper (PECI1)
3	2 x 288-pin DDR5 DIMM Slots (DDR5_A1, DDR5_C1)*
4	2 x 288-pin DDR5 DIMM Slots (DDR5_B1, DDR5_D1)*
5	PSU SMBus Header (PSU_SMB1)
6	Micro-Fit ATX 4Pin Power Connector (ATX4PIN1)
7	Mini Cool Edge IO x8 Connector (MCIO1)
8	Mini Cool Edge IO x8 Connector (MCIO2)
9	PWM Configuration Header (PWM_CFG1)
10	LGA 4677 CPU Socket (CPU0)
11	ATX 12V Power Connector (ATX12V1)
12	ATX 12V Power Connector (ATX12V2)
13	ATX 12V Power Connector (ATX12V3)
14	2 x 288-pin DDR5 DIMM Slots (DDR5_E1, DDR5_G1)*
15	2 x 288-pin DDR5 DIMM Slots (DDR5_F1, DDR5_H1)*
16	Micro-Fit SATA Power Connector (SATAPWR1)
17	Clear CMOS Pad (CLRMOS1)
18	System Fan Connector (FAN1)
19	System Fan Connector (FAN2)
20	System Fan Connector (FAN3)
21	System Fan Connector (FAN4)
22	System Fan Connector (FAN5)
23	System Fan Connector (FAN6)
24	MiniSAS HD SATA/PCIE Selection Jumper (MINISAS_2)
25	MiniSAS HD SATA/PCIE Selection Jumper (MINISAS_1)
26	MiniSAS HD Connector (MSAS_HD0_7)
27	Speaker Header (SPEAKER1)
28	USB 2.0 Header (USB1_2) (SPC741D8UD-2T/X550 only)
29	System Panel Header (PANEL1)
30	Auxiliary Panel Header (AUX_PANEL1)
31	Non Maskable Interrupt Button (NMI_BTN1)
32	Thermal Sensor Header (TR1)
33	Password Reset Jumper (PASSWORD_CLEAR1)

No.	Description
34	Virtual RAID On CPU Header (RAID_1)
35	SPI TPM Header (TPM_BIOS_PH1)
36	M-key M.2 Socket (M2_1) (Type 2280)
37	ME Recovery Jumper (ME_RECOVERY1)
38	Intelligent Platform Management Bus Header (IPMB_1)
39	BMC SMBus Header (BMC_SMB_2) (SPC741D8UD-2T/X550 only)
40	BMC SMBus Header (BMC_SMB_1)
41	Serial Port Header (COM1)
42	PCI Express 5.0 x16 Slot (PCIE4)
43	PCI Express 5.0 x16 Slot (PCIE5)
44	PCI Express 5.0 x16 Slot (PCIE6)
45	PCI Express 5.0 x16 Slot (PCIE7)

 $<sup>^*</sup>$ For DIMM installation and configuration instructions, please see p.22 (Installation of Memory Modules (DIMM)) for more details.

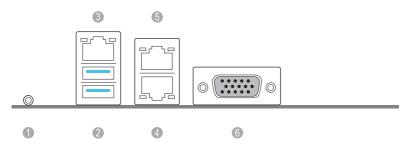
# 1.5 Onboard LED Indicators



No.	ltem	Status	Description	
1	LED_FAN1	Red	FAN1 failed	
2	LED_FAN2	Red	FAN2 failed	
3	LED_FAN3	Red	FAN3 failed	
4	LED_FAN4	Red	FAN4 failed	
5	LED_FAN5	Red	FAN5 failed	
6	LED_FAN6	Red	FAN6 failed	
7	SB_PWR1	Green	STB PWR ready	
8	BMC_LED1	Green	BMC heartbeat LED	
9	CATERR	Red	CPU CATERR error	

# 1.6 I/O Panel

## SPC741D8UD-2T / SPC741D8UD-2T/X550:



No.	Description	No.	Description
1	UID Switch (UID1)	4	10G LAN RJ-45 Port (LAN1, shared NIC)**
2	USB 3.2 Gen1 Ports (USB3_1_2)	5	10G LAN RJ-45 Port (LAN2)**
3	LAN RJ-45 Port (IPMI_LAN1)*	6	VGA Port (VGA1)

#### **LAN Port LED Indications**

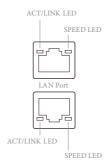
\*There are two LEDs on each LAN port. Please refer to the table below for the LAN port LED indications.



#### **Dedicated IPMI LAN Port LED Indications**

Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No Link	Off	10Mbps connection or
			no link
Blinking Yellow	Data Activity	Yellow	100Mbps connection
On	Link	Green	1Gbps connection

\*\*There are two LEDs on each LAN port. Please refer to the table below for the LAN port LED indications.

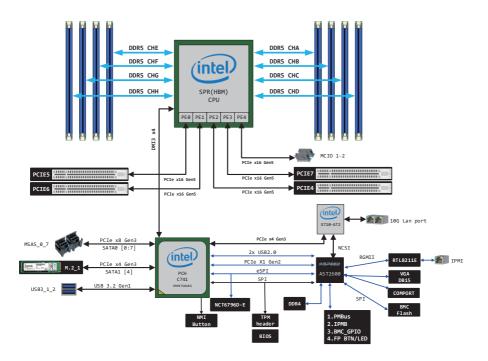


#### **LAN Port LED Indications**

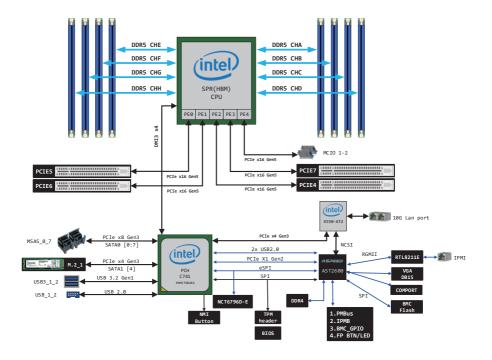
Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No Link	Off	100Mbps connection or no
			link
Blinking Yellow	Data Activity	Orange	1Gbps connection
On	Link	Green	10Gbps connection

# 1.7 Block Diagram

## SPC741D8UD-2T:



#### SPC741D8UD-2T/x550:



# **Chapter 2 Installation**

This is a deep micro-ATX form factor  $(10.4^{\circ} \times 10.5^{\circ})$  motherboard. Before you install the motherboard, study the configuration of your chassis to ensure that the motherboard fits into it.



Make sure to unplug the power cord before installing or removing the motherboard. Failure to do so may cause physical injuries to you and damages to motherboard components.

### 2.1 Screw Holes

Place screws into the holes indicated by circles to secure the motherboard to the chassis.



Attention! Before installing this motherboard, be sure to unscrew and remove the standoff at the marked location, under the motherboard, from the chassis, in order to avoid electrical short circuit and damage to your motherboard.



Do not over-tighten the screws! Doing so may damage the motherboard.

#### 2.2 Pre-installation Precautions

Take note of the following precautions before you install motherboard components or change any motherboard settings.

- 1. Unplug the power cord from the wall socket before touching any components.
- To avoid damaging the motherboard's components due to static electricity, NEVER place your motherboard directly on the carpet or the like. Also remember to use a grounded wrist strap or touch a safety grounded object before you handle the components.
- 3. Hold components by the edges and do not touch the ICs.
- 4. Whenever you uninstall any component, place it on a grounded anti-static pad or in the bag that comes with the component.
- 5. When placing screws into the screw holes to secure the motherboard to the chassis, please do not over-tighten the screws! Doing so may damage the motherboard.



Before you install or remove any component, ensure that the power is switched off or the power cord is detached from the power supply. Failure to do so may cause severe damage to the motherboard, peripherals, and/or components.

# 2.3 Installing the CPU and Heatsink

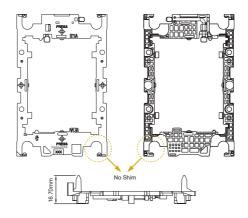


- $1. \ \ Unplug \ all \ power \ cables \ before \ installing \ the \ CPU.$
- 2. Illustration in this documentation are examples only.

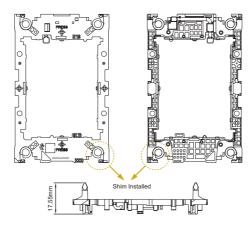
#### Carrier Used

Carrier Type	Xeon® SP XCC	Xeon® SP MCC/LCC
Carrier Code	E1A	E1B
Shim	No	Yes
Carrier Height	16.70mm	17.55mm

#### **XCC Carrier**

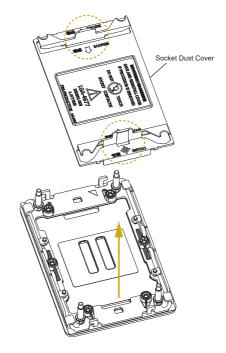


#### **MCC Carrier**

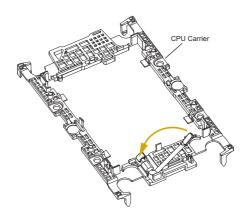


Follow the steps below to finish the CPU installation and please save the Socket Dust Cover when returning for service.

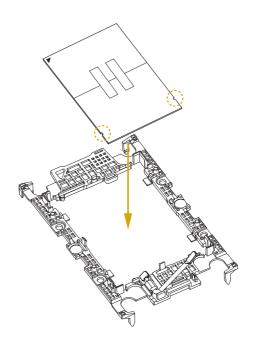




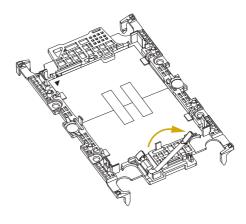




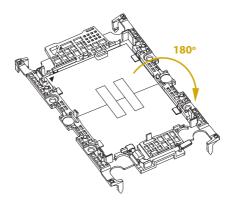




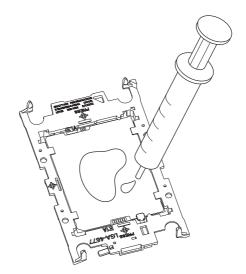




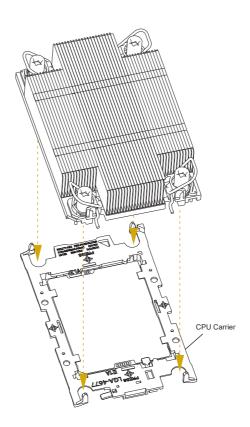




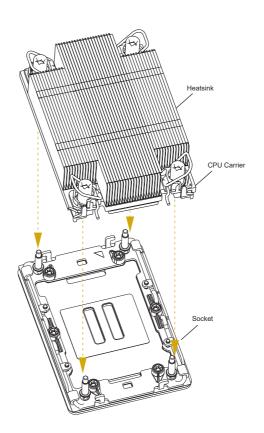




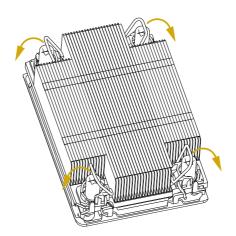




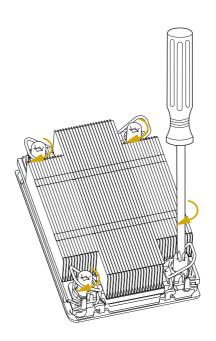












# 2.4 Installing Memory Modules (DIMM)

This motherboard provides eight 288-pin DDR5 (Double Data Rate 5) DIMM slots and supports Single Channel Memory Technology.

#### CPU0

DDR5\_A1, B1, C1, D1, E1, F1, G1 H1



- For Eight channel configuration, you always need to install identical (the same brand, speed, size and chip-type) DDR5 DIMM groups.
- 2. Single Channel Memory Technology is enabled only when 8 memory modules are installed.
- 3. It is not allowed to install a DDR, DDR2, DDR3 or DDR4 memory module into a DDR5 slot; otherwise, this motherboard and DIMM may be damaged.

## 2.4.1 Memory Support

#### 4th Gen Intel® Xeon® Scalable Processors - SP

Torres	Ranks Per DIMM		l Density		Speed (MT/s); Voltage (V); DIMM Per Channel (DPC)		
Type	and Data Width	DIMM Capacity		1DPC <sup>1</sup>	2DPC		
		16GB	$24GB^2$	32GB	1.	1V	
	SRx8 (RC D)	16GB	24GB	NA			
	SRx4 (RC C)	32GB	48GB	NA			
RDIMM	SRx4 (RC F) 9x4	32GB	NA	NA		4400	
KUIIVIIVI	DRx8 (RC E)	32GB	48GB	NA	4000		
	DRx4 (RC A)	64GB	96GB	128GB	4800		
	DRx4 (RC B) 9x4	64GB	NA	NA			
RDIMM- 3DS	(4R/8R) x4 (RC A)	2H-128GB 4H-256GB	NA	NA			

Note1: 1DPC applies to 1 SPC or 2SPC implementations (SPC-Sockets Per Channel).

Note2: 24GB XCC only w/ limited configs: 1DPC all DIMM types, 2DPC 96GB only. Only 8 and 16 DIMM configs, no fallbacks.

Note3: The memory speed will be 4800 MT/s 1DPC and 4400 MT/s 2DPC.

Note4: The table is for reference only.

## 5<sup>th</sup> Gen Intel® Xeon® Scalable Processors - SP

Туре	Ranks Per DIMM and Data Width		l Densit I Capaci		Speed (MT/s); Voltage (V); DIMM Per Channel (DPC) 1DPC <sup>1</sup> 2DPC		
		16GB	24GB	32GB	1.1	1V	
	SRx8 (RC D)	16GB	24GB <sup>2</sup>	NA			
	SRx4 (RC C)	32GB	48GB <sup>2</sup>	NA		44003	
RDIMM	SRx4 (RC F) 9x4	NA	NA	NA	5600 <sup>3</sup>		
KDIIVIIVI	DRx8 (RC E)	32GB	48GB <sup>2</sup>	NA	3600		
	DRx4 (RC A)	64GB	96GB	128GB		4400³	
	DRx4 (RC B) 9x4	NA	NA	NA			
RDIMM- 3DS	(4R/8R) x4 (RC A)	H-128GB 4H-256GB	NA	NA	5600 <sup>4</sup>		

Note1: 1DPC applies to 1 SPC or 2SPC implementations (SPC-Sockets Per Channel).

Note2: 24GB 2DPC not POR w/ 24GB and 48GM DIMMs.

Note3: DDR5-5600 RDIMMs will be limited to 5600 MT/s 1DPC and 4400 MT/s 2DPC. DDR5-4800 DIMMs will be limited to 4800 MT/s 1DPC and 4400 MT/s 2DPC.

Note4: DDR5-5600 DIMM are required for 5600 and 5200 1DPC speeds.

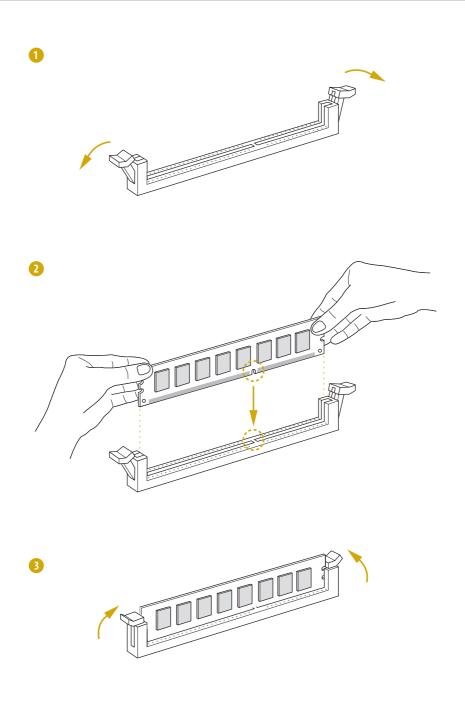
Note5: EE LCC DDR5 memory support POR is 16GB/24GB/32GB at 4400 for 1DPC and 2DPC.

Note6: The table is for reference only.

## 2.4.2 Memory Configurations

DIMM Slot		DIMM Number											
511411	,, 5100												
CPU0	A1	V				V		V	V	V		V	V
	B1			V						V	V	V	V
	C1						V	V	V	V	V		V
	D1								V		V	V	V
	E1		V				V	V	V	V	V		V
	F1				V				V		V	V	V
	G1					V		V	V	V		V	V
	H1									V	V	V	V

The symbol V indicates the slot is populated.



# 2.5 Expansion Slots (PCI Express Slots)

There are 4 PCI Express slots on this motherboard.

#### PCIE slots:

PCIE4 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards. PCIE5 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards. PCIE6 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards. PCIE7 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards.

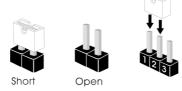
Slot	Generation	Mechanical	Electrical	Source
PCIE4	5.0	x16	x16	CPU0
PCIE5	5.0	x16	x16	CPU0
PCIE6	5.0	x16	x16	CPU0
PCIE7	5.0	x16	x16	CPU0

### Installing an expansion card

- Step 1. Before installing an expansion card, please make sure that the power supply is switched off or the power cord is unplugged. Please read the documentation of the expansion card and make necessary hardware settings for the card before you start the installation.
- Step 2. Remove the system unit cover (if your motherboard is already installed in a chassis).
- Step 3. Remove the bracket facing the slot that you intend to use. Keep the screws for later use.
- Step 4. Align the card connector with the slot and press firmly until the card is completely seated on the slot.
- Step 5. Fasten the card to the chassis with screws.
- Step 6. Replace the system cover.

# 2.6 Jumper Setup

The illustration shows how jumpers are setup. When the jumper cap is placed on the pins, the jumper is "Short". If no jumper cap is placed on the pins, the jumper is "Open". The illustration shows a 3-pin jumper whose pin1 and pin2 are "Short" when a jumper cap is placed on these 2 pins.



ME Recovery Jumper (3-pin ME_RECOVERY1) (see p.6, No. 37)	1_2 Normal Mode (Default)	<b>2_3</b> ○ • • • • • • • • • • • • • • • • • • •
CPU PECI Mode Jumper (3-pin PECI1) (see p.6, No. 2)	1_2 CPU PECI connected to PCH	2_3  CPU PECI connected to  BMC (Default)
MiniSAS HD SATA/PCIE Selection Jumper (3-pin MINISAS_2) (see p.6, No. 24)	<b>1_2</b> ■ ■ ● ○  SATA (Default)	2_3 ○ • • PCIE
MiniSAS HD SATA/PCIE Selection Jumper (3-pin MINISAS_1) (see p.6, No. 25)	1_2 SATA (Default)	2_3 ○ • • • PCIE

English

The illustration shows how jumpers are setup. When the jumper cap is placed on the pins, the jumper is "Short". If no jumper cap is placed on the pins, the jumper is "Open".



Password Reset Jumper (2-pin PASSWORD\_ CLEAR) (see p.6, No. 33)



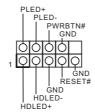
Open: Normal Mode (Default) Short: Password Clear

#### 2.7 Onboard Headers and Connectors



Onboard headers and connectors are NOT jumpers. Do NOT place jumper caps over these headers and connectors. Placing jumper caps over the headers and connectors will cause permanent damage to the motherboard.

System Panel Header (9-pin PANEL1) (see p.6, No. 29)



Connect the power switch, reset switch and system status indicator on the chassis to this header according to the pin assignments. Particularly note the positive and negative pins before connecting the cables.



#### PWRBTN (Power Switch):

Connect to the power switch on the chassis front panel. You may configure the way to turn off your system using the power switch.

#### RESET (Reset Switch):

Connect to the reset switch on the chassis front panel. Press the reset switch to restart the computer if the computer freezes and fails to perform a normal restart.

#### PLED (System Power LED):

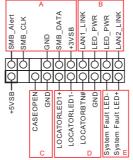
Connect to the power status indicator on the chassis front panel. The LED is on when the system is operating. The LED is off when the system is in S4 sleep state or powered off (S5).

#### HDLED (Hard Drive Activity LED):

Connect to the hard drive activity LED on the chassis front panel. The LED is on when the hard drive is reading or writing data.

The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

Auxiliary Panel Header (18-pin AUX\_PANEL1) (see p.6, No. 30)



This header supports multiple functions on the front panel, including the front panel SMB, internet status indicator and chassis intrusion pin.



#### A. Front panel SMBus connecting pin (6-1 pin FPSMB)

This header allows you to connect SMBus (System Management Bus) equipment. It can be used for communication between peripheral equipment in the system, which has slower transmission rates, and power management equipment.

#### B. Internet status indicator (2-pin LAN1\_LED, LAN2\_LED)

These two 2-pin headers allow you to use the Gigabit internet indicator cable to connect to the LAN status indicator. When this indicator flickers, it means that the internet is properly connected.

#### C. Chassis intrusion pin (2-pin CHASSIS)

This header is provided for host computer chassis with chassis intrusion detection designs. In addition, it must also work with external detection equipment, such as a chassis intrusion detection sensor or a microswitch. When this function is activated, if any chassis component movement occurs, the sensor will immediately detect it and send a signal to this header, and the system will then record this chassis intrusion event. The default setting is set to the CASEOPEN and GND pin; this function is off.

#### D. Locator LED (4-pin LOCATOR)

This header is for the locator switch and LED on the front panel.

#### E. System Fault LED (2-pin LOCATOR)

This header is for the Fault LED on the system.

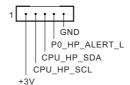
MiniSAS HD Connector Right-Angle: (MSAS\_HD0\_7) (see p.6, No. 26)



MSAS\_HD0\_7

This connector supports MiniSAS-to-SATA data cables for internal storage devices with up to 6.0 Gb/s data transfer rate.

Backplane PCI Express Hot-Plug Connector (5-pin CPU1\_ HSBP1) (see p.6, No. 1)



This header is used for the hot plug feature of HDDs on the backplane.

Thermal Sensor Header (3-pin TR1) (see p.6, No. 32)



Please connect the thermal sensor cable to either pin 1-2 or pin 2-3 and the other end to the device which you wish to monitor its temperature.

PWM Configuration Header (3-pin PWM\_CFG1) (see p.6, No. 9)



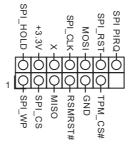
The header is used for PWM configurations.

Non Maskable Interrupt Button Header (NMI\_BTN1) (see p.6, No. 31)



Please connect a NMI device to this header.

SPI TPM Header (13-pin TPM\_BIOS\_PH1) (see p.6, No. 35)



This connector supports SPI Trusted Platform Module (TPM) system, which can securely store keys, digital certificates, passwords, and data. A TPM system also helps enhance network security, protects digital identities, and ensures platform integrity.

# Enalis

# SPC741D8UD-2T/X550

## only:

USB 2.0 Header (9-pin USB1\_2) (see p.6, No. 28)

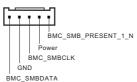


There is one USB 2.0 header on this motherboard. This USB 2.0 header can support two ports.

BMC SMB Headers

 $(5-pin\ BMC\_SMB\_1)$ 

(see p.6, No. 40)



These headers are used for the SM BUS devices.

# SPC741D8UD-2T/X550

### only:

(5-pin BMC\_SMB\_2) (see p.6, No. 39)

Clear CMOS Pad (CLRMOS1)

(see p.6, No. 17)



This allows you to clear the data in CMOS. To clear CMOS, take out the CMOS battery and short the Clear CMOS Pad.

System Fan Connectors

(6-pin FAN1)

(see p.6, No. 18)

(6-pin FAN2)

(see p.6, No. 19)

(6-pin FAN3)

(see p.6, No. 20)

(6-pin FAN4)

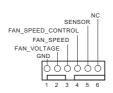
(see p.6, No. 21)

(6-pin FAN5)

(see p.6, No. 22)

(6-pin FAN6)

(see p.6, No. 23)



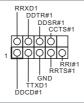
Please connect fan cables to the fan connectors and match the black wire to the ground pin.
All fans support Fan Control.

Micro-Fit SATA Power Connector(DC-IN mode) (6-pin SATAPWR1) (see p.6, No. 16)



Please use a SATA power cable to connect this SATA Power Connector and your SATA HDD for supplying power from the motherboard, when using DC-IN mode without SATA power supply.

Serial Port Header (9-pin COM1) (see p.6, No. 41)



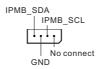
This COM1 header supports a serial port module.

Chassis Speaker Header (4-pin SPEAKER1) (see p.6, No. 27)



Please connect the chassis speaker to this header.

Intelligent Platform Management Bus Header (4-pin IPMB\_1) (see p.6, No. 38)



This 4-pin connector is used to provide a cabled base-board or front panel connection for value added features and 3rd-party add-in cards, such as Emergency Management cards, that provide management features using the IPMB.

PSU SMBus Header (5-pin PSU\_SMB1) (see p.6, No. 5)



PSU SMBus monitors the status of the power supply, fan and system temperature.

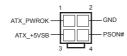
Enalish

ATX 12V Power Connectors (8-pin ATX12V1) (see p.6, No. 11) (8-pin ATX12V2) (see p.6, No. 12) (8-pin ATX12V3) (see p.6, No. 13)



This motherboard provides three 8-pin ATX 12V power connectors.

Micro-Fit ATX 4Pin Power Connector (4-pin ATX4PIN1 (ATX 24pin-to-4pin)) (see p.6, No. 6)

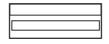


The motherboard provides one 4-pin power/signal connector which is a required input for ATX power source.

When using ATX power, it is necessary to use a 24pin-to-4pin power cable to connect between the 24pin power connector of PSU and the ATX12V1, ATX12V2 or ATX12V3 connector on the motherboard for power supply and signal communication.

For DC-IN 12V application, it is not necessary to use this ATX 4-PIN power connector.

Mini Cool Edge IO x8 Connectors (MCIO1) (see p.6, No. 7) (MCIO2) (see p.6, No. 8)



This motherboard supports two Mini Cool Edge IO x8 Connectors. Please connect these connectors to the HDD backplane board.

# **MCIO1 Pin Definition**

Pin         Defeinition           A1         GND         B1         GND           A2         RX_DP0         B2         TX_DP0           A3         RX_DN0         B3         TX_DN0           A4         GND         B4         GND           A5         RX_DP1         B5         TX_DP1           A6         RX_DN1         B6         TX_DN1           A7         GND         B7         GND           A8         SPARE_A1         B8         SCL1           A9         WAKE_N         B9         SDA1           A10         GND         B10         GND           A11         DP         B11         RESET_N           A12         DN         B12         PRSNT0_N           A13         GND         B13         GND           A14         RX_DP2         B14         TX_DP2           A15         RX_DN2         B15         TX_DN2           A16         GND         B16         GND           A17         RX_DP3         B17         TX_DP3           A18         RX_DN3         B18         TX_DN3           A19         GND         B19
A3       RX_DN0       B3       TX_DN0         A4       GND       B4       GND         A5       RX_DP1       B5       TX_DP1         A6       RX_DN1       B6       TX_DN1         A7       GND       B7       GND         A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23 <t< td=""></t<>
A4       GND       B4       GND         A5       RX_DP1       B5       TX_DP1         A6       RX_DN1       B6       TX_DN1         A7       GND       B7       GND         A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24
A5       RX_DP1       B5       TX_DP1         A6       RX_DN1       B6       TX_DN1         A7       GND       B7       GND         A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24
A6       RX_DN1       B6       TX_DN1         A7       GND       B7       GND         A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26
A7       GND       B7       GND         A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B28
A8       SPARE_A1       B8       SCL1         A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28
A9       WAKE_N       B9       SDA1         A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A10       GND       B10       GND         A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A11       DP       B11       RESET_N         A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A12       DN       B12       PRSNT0_N         A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A13       GND       B13       GND         A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A14       RX_DP2       B14       TX_DP2         A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A15       RX_DN2       B15       TX_DN2         A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A16       GND       B16       GND         A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A17       RX_DP3       B17       TX_DP3         A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A18       RX_DN3       B18       TX_DN3         A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A19       GND       B19       GND         A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A20       RX_DP4       B20       TX_DP4         A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A21       RX_DN4       B21       TX_DN4         A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A22       GND       B22       GND         A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A23       RX_DP5       B23       TX_DP5         A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A24       RX_DN5       B24       TX_DN5         A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A25       GND       B25       GND         A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A26       SPARE_A2       B26       SCL2         A27       WAKE_N       B27       SDA2         A28       GND       B28       GND
A27 WAKE_N B27 SDA2 A28 GND B28 GND
A28 GND B28 GND
A29 DP B29 RESET_N
A30 DN B30 PRSNT1_N
A31 GND B31 GND
A32 RX_DP6 B32 TX_DP6
A33 RX_DN6 B33 TX_DN6
A34 GND B34 GND
A35 RX_DP7 B35 TX_DP7
A36 RX_DN7 B36 TX_DN7
A37 GND B37 GND
75 NP_NC_1 76 NP_NC_2
77 PGND_1 78 PGND_3
77 FGND_1 78 FGND_3  79 PGND_2 80 PGND_4

# **MCIO2** Pin Definition

Pin	Defeinition	Pin	Defeinition
A1	GND	B1	GND
A2	RX_DP8	B2	TX_DP8
A3	RX_DN8	В3	TX_DN8
A4	GND	B4	GND
A5	RX_DP9	B5	TX_DP9
A6	RX_DN9	В6	TX_DN9
A7	GND	В7	GND
A8	SPARE_A1	B8	SCL1
A9	WAKE_N	В9	SDA1
A10	GND	B10	GND
A11	DP	B11	RESET_N
A12	DN	B12	PRSNT2_N
A13	GND	B13	GND
A14	RX_DP10	B14	TX_DP10
A15	RX_DN10	B15	TX_DN10
A16	GND	B16	GND
A17	RX_DP11	B17	TX_DP11
A18	RX_DN11	B18	TX_DN11
A19	GND	B19	GND
A20	RX_DP12	B20	TX_DP12
A21	RX_DN12	B21	TX_DN12
A22	GND	B22	GND
A23	RX_DP13	B23	TX_DP13
A24	RX_DN13	B24	TX_DN13
A25	GND	B25	GND
A26	SPARE_A2	B26	SCL2
A27	WAKE_N	B27	SDA2
A28	GND	B28	GND
A29	DP	B29	RESET_N
A30	DN	B30	PRSNT3_N
A31	GND	B31	GND
A32	RX_DP14	B32	TX_DP14
A33	RX_DN14	B33	TX_DN14
A34	GND	B34	GND
A35	RX_DP15	B35	TX_DP15
A36	RX_DN15	B36	TX_DN15
A37	GND	B37	GND
75	NP_NC_1	76	NP_NC_2
77	PGND_1	78	PGND_3
79	PGND_2	80	PGND_4

Virtual RAID On CPU  Header  VROC RAID KEY  (4-pin RAID_1)  (see p.6, No. 34)  This connector sup Virtual RAID on C  NVME/AHCI RAI  GND  PCIE.	
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With the introduction of the Intel VROC product, there are three modes of operation:

SKU	HW key required	Key features
Pass-thru	Not needed	<ul> <li>Pass-thru only (no RAID)</li> <li>LED Management</li> <li>Hot Plug Support</li> <li>RAID 0 support for Intel Fultondale NVMe SSDs</li> </ul>
Standard	VROCSTANMOD	<ul><li>Pass-thru SKU features</li><li>RAID 0, 1, 10</li></ul>
Premium ISS	VROCPREMMOD  VROCISSDMOD	<ul> <li>Standard SKU features</li> <li>RAID 5</li> <li>RAID 5 Write Hole Closure</li> </ul>

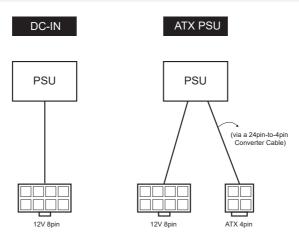
<sup>\*</sup>Only Intel SSDs are supported.

<sup>\*</sup>For further details on VROC, please refer to the official information released by Intel.

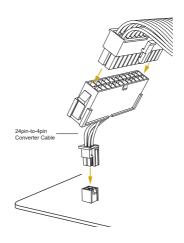
# 2.8 ATX PSU / DC-IN Power Connections

This motherboard supports both +12V DC and ATX power input. Please refer to the table below for the required connections between the motherboard and the power supply.

Connector	DC-IN	ATX PSU
12V 8pin	O	O
ATX 4pin	X	O (with the bundled ATX 24pin-to-4pin converter cable)



The following diagram illustrates how to connect the bundled ATX 24pin-to-4pin converter cable.



# 2.9 Dr. Debug

Dr. Debug is used to provide code information, which makes troubleshooting even easier. Please see the diagrams below for reading the Dr. Debug codes.

Code	Description
0x10	PEI_CORE_STARTED
0x11	PEI_CAR_CPU_INIT
0x15	PEI_CAR_NB_INIT
0x19	PEI_CAR_SB_INIT
0x31	PEI_MEMORY_INSTALLED
0x32	PEI_CPU_INIT
0x33	PEI_CPU_CACHE_INIT
0x34	PEI_CPU_AP_INIT
0x35	PEI_CPU_BSP_SELECT
0x36	PEI_CPU_SMM_INIT
0x37	PEI_MEM_NB_INIT
0x3B	PEI_MEM_SB_INIT
0x4F	PEI_DXE_IPL_STARTED
0x60	DXE_CORE_STARTED
0x61	DXE_NVRAM_INIT
0x62	DXE_SBRUN_INIT

0x63	DXE_CPU_INIT
0x68	DXE_NB_HB_INIT
0x69	DXE_NB_INIT
0x6A	DXE_NB_SMM_INIT
0x70	DXE_SB_INIT
0x71	DXE_SB_SMM_INIT
0x72	DXE_SB_DEVICES_INIT
0x78	DXE_ACPI_INIT
0x79	DXE_CSM_INIT
0x90	DXE_BDS_STARTED
0x91	DXE_BDS_CONNECT_DRIVERS
0x92	DXE_PCI_BUS_BEGIN
0x93	DXE_PCI_BUS_HPC_INIT
0x94	DXE_PCI_BUS_ENUM
0x95	DXE_PCI_BUS_REQUEST_RESOURCES
0x96	DXE_PCI_BUS_ASSIGN_RESOURCES
0x97	DXE_CON_OUT_CONNECT
0x98	DXE_CON_IN_CONNECT

0x99	DXE_SIO_INIT
0x9A	DXE_USB_BEGIN
0x9B	DXE_USB_RESET
0x9C	DXE_USB_DETECT
0x9D	DXE_USB_ENABLE
0xA0	DXE_IDE_BEGIN
0xA1	DXE_IDE_RESET
0xA2	DXE_IDE_DETECT
0xA3	DXE_IDE_ENABLE
0xA4	DXE_SCSI_BEGIN
0xA5	DXE_SCSI_RESET
0xA6	DXE_SCSI_DETECT
0xA7	DXE_SCSI_ENABLE
0xA8	DXE_SETUP_VERIFYING_PASSWORD
0xA9	DXE_SETUP_START
0xAB	DXE_SETUP_INPUT_WAIT
0xAD	DXE_READY_TO_BOOT
0xAE	DXE_LEGACY_BOOT

0xAF	DXE_EXIT_BOOT_SERVICES
0xB0	RT_SET_VIRTUAL_ADDRESS_MAP_BEGIN
0xB1	RT_SET_VIRTUAL_ADDRESS_MAP_END
0xB2	DXE_LEGACY_OPROM_INIT
0xB3	DXE_RESET_SYSTEM
0xB4	DXE_USB_HOTPLUG
0xB5	DXE_PCI_BUS_HOTPLUG
0xB6	DXE_NVRAM_CLEANUP
0xB7	DXE_CONFIGURATION_RESET
0xF0	PEI_RECOVERY_AUTO
0xF1	PEI_RECOVERY_USER
0xF2	PEI_RECOVERY_STARTED
0xF3	PEI_RECOVERY_CAPSULE_FOUND
0xF4	PEI_RECOVERY_CAPSULE_LOADED
0xE0	PEI_S3_STARTED
0xE1	PEI_S3_BOOT_SCRIPT
0xE2	PEI_S3_VIDEO_REPOST

0xE3	PEI_S3_OS_WAKE
0x50	PEI_MEMORY_INVALID_TYPE
0x53	PEI_MEMORY_NOT_DETECTED
0x55	PEI_MEMORY_NOT_INSTALLED
0x57	PEI_CPU_MISMATCH
0x58	PEI_CPU_SELF_TEST_FAILED
0x59	PEI_CPU_NO_MICROCODE
0x5A	PEI_CPU_ERROR
0x5B	PEI_RESET_NOT_AVAILABLE
0xD0	DXE_CPU_ERROR
0xD1	DXE_NB_ERROR
0xD2	DXE_SB_ERROR
0xD3	DXE_ARCH_PROTOCOL_NOT_AVAILABLE
0xD4	DXE_PCI_BUS_OUT_OF_RESOURCES
0xD5	DXE_LEGACY_OPROM_NO_SPACE
0xD6	DXE_NO_CON_OUT
0xD7	DXE_NO_CON_IN

0xD8	DXE_INVALID_PASSWORD
0xD9	DXE_BOOT_OPTION_LOAD_ERROR
0xDA	DXE_BOOT_OPTION_FAILED
0xDB	DXE_FLASH_UPDATE_FAILED
0xDC	DXE_RESET_NOT_AVAILABLE
0xE8	PEI_MEMORY_S3_RESUME_FAILED
0xE9	PEI_S3_RESUME_PPI_NOT_FOUND
0xEA	PEI_S3_BOOT_SCRIPT_ERROR
0xEB	PEI_S3_OS_WAKE_ERROR

# 2.10 Unit Identification purpose LED/Switch

With the UID button, You are able to locate the server you're working on from behind a rack of servers.

Unit Identification purpose LED/Switch (UID1)



When the UID button on the front or rear panel is pressed, the front/rear UID blue LED indicator will be turned on. Press the UID button again to turn off the indicator.

# English

# 2.11 Dual LAN and Teaming Operation Guide

Dual LAN with Teaming enabled on this motherboard allows two single connections to act as one single connection for twice the transmission bandwidth, making data transmission more effective and improving the quality of transmission of distant images. Fault tolerance on the dual LAN network prevents network downtime by transferring the workload from a failed port to a working port.



The speed of transmission is subject to the actual network environment or status even with Teaming enabled.

Before setting up Teaming, please make sure whether the Switch (or Router) supports Teaming (IEEE 802.3ad Link Aggregation). specify a preferred adapter in Intel PROSet. Under normal conditions, the Primary adapter handles all non-TCP/IP traffic. The Secondary adapter will receive fallback traffic if the primary fails. If the Preferred Primary adapter fails, but is later restored to an active status, control is automatically switched back to the Preferred Primary adapter.

#### Step 1

From Device Manager, open the properties of a team.

#### Step 2

Click the **Settings** tab.

#### Step 3

Click the **Modify Team** button.

#### Step 4

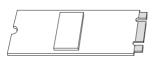
Select the adapter that wants to be the primary adapter and click the Set Primary button.

The software will choose an adapter of the highest capability (model and speed) to act as the default primary upon not specify a preferred primary adapter. If a failover occurs, another adapter becomes the primary. The adapter will, however, rejoin the team as a non-primary.

# 2.12 M.2 SSD Module Installation Guide

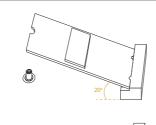
The M.2 Socket (M2\_1, Key M) supports type 2280 SATA3 6.0 Gb/s module or a M.2 PCI Express module up to Gen3 x4 (8GT/s x4).

# Installing the M.2 SSD Module



#### Step 1

Prepare a M.2 SSD module and the screw.



#### Step 2

Gently insert the M.2 SSD module into the M.2 slot. Please be aware that the M.2 SSD module only fits in one orientation.



#### Step 3

Tighten the screw with a screwdriver to secure the module into place. Please do not overtighten the screw as this might damage the module.

# **Chapter 3 UEFI Setup Utility**

## 3.1 Introduction

This section explains how to use the UEFI SETUP UTILITY to configure your system. The UEFI chip on the motherboard stores the UEFI SETUP UTILITY. You may run the UEFI SETUP UTILITY when you start up the computer. Please press <F2> or <Del> during the Power-On-Self-Test (POST) to enter the UEFI SETUP UTILITY; otherwise, POST will continue with its test routines.

If you wish to enter the UEFI SETUP UTILITY after POST, restart the system by pressing <Ctrl> + <Alt> + <Delete>, or by pressing the reset button on the system chassis. You may also restart by turning the system off and then back on.



Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions are for reference purpose only, and they may not exactly match what you see on your screen.

#### 3.1.1 UFFI Menu Bar

The top of the screen has a menu bar with the following selections:

Item	Description
Main	To set up the system time/date information
Advanced	To set up the advanced UEFI features
Server Mgmt	To manage the server
Security	To set up the security features
Boot	To set up the default system device to locate and load the Operating System
Event Logs	For event log configuration
Exit	To exit the current screen or the UEFI SETUP UTILITY

Use <  $\leftarrow$  > key or <  $\rightarrow$  > key to choose among the selections on the menu bar, and then press <Enter> to get into the sub screen.

# 3.1.2 Navigation Keys

Please check the following table for the function description of each navigation key.

Navigation Key(s)	Function Description
<b>←</b> / <b>→</b>	Moves cursor left or right to select Screens
<b>↑</b> / <b>↓</b>	Moves cursor up or down to select items
+ / -	To change option for the selected items
<tab></tab>	Switch to next function
<enter></enter>	To bring up the selected screen
<pgup></pgup>	Go to the previous page
<pgdn></pgdn>	Go to the next page
<home></home>	Go to the top of the screen
<end></end>	Go to the bottom of the screen
<f1></f1>	To display the General Help Screen
<f7></f7>	Discard changes and exit the UEFI SETUP UTILITY
<f9></f9>	Load optimal default values for all the settings
<f10></f10>	Save changes and exit the UEFI SETUP UTILITY
<f12></f12>	Print screen
<esc></esc>	Jump to the Exit Screen or exit the current screen

# 3.2 Main Screen

Once you enter the UEFI SETUP UTILITY, the Main screen will appear and display the system overview. The Main screen provides system overview information and allows you to set the system time and date.



## 3.2.1 Mother Board Information

Press <Enter> to view the information of the motheboard.



## 3.2.2 Processor Information

Press <Enter> to view the information of the processor.



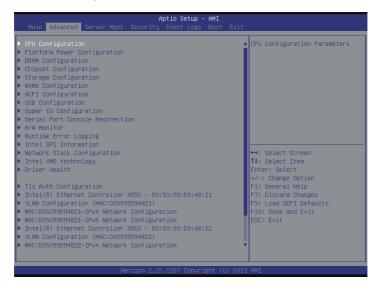
# 3.2.3 Memory Information

Press <Enter> to view the information of the memory.



# 3.3 Advanced Screen

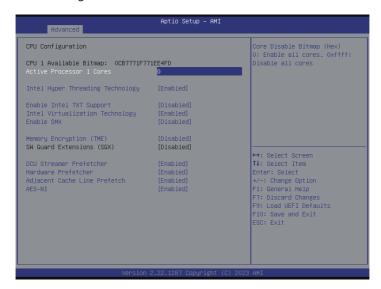
In this section, you may set the configurations for the following items: CPU Configuration, Platform Power Configuration, DRAM Configuration, Chipset Configuration, Storage Configuration, NVME Configuration, ACPI Configuration, USB Configuration, Super IO Configuration, Serial Port Console Redirection, H/W Monitor, Runtime Error Logging, Intel SPS Information, Network Stack Configuration, Intel VMD Technology, Driver Health, Tls Auth Configuration and Instant Flash.





Setting wrong values in this section may cause the system to malfunction.

# 3.3.1 CPU Configuration



#### **Active Processor 1 Cores**

Select the number of cores to enable in each processor package.

# Intel Hyper Threading Technology

Intel Hyper Threading Technology allows multiple threads to run on each core, so that the overall performance on threaded software is improved.

# **Enable Intel TXT Support**

Enables Intel Trusted Execution Technology Configuration.

# Intel Virtualization Technology

Intel Virtualization Technology allows a platform to run multiple operating systems and applications in independent partitions, so that one computer system can function as multiple virtual systems.

#### **Fnable SMX**

Use this item to enable Safer Mode Extensions.

# Memory Encryption (TME)

Use this item to enable or disable Memory Encryption (TME).

# SW Guard Extensions (SGX)

Use this item to enable or disable Software Guard Extensions (SGX).

## DCU Streamer Prefetcher

DCU streamer prefetcher is an L1 data cache prefetcher (MSR 1A4h [2]).

## Hardware Prefetcher

Automatically prefetch data and code for the processor. Enable for better performance.

# Adjacent Cache Line Prefetch

Automatically prefetch the subsequent cache line while retrieving the currently requested cache line. Enable for better performance.

#### AFS-NI

Use this item to enable or disable AES-NI support.

# 3.3.2 Platform Power Configuration



## Intel SpeedStep Technology

Intel SpeedStep technology allows processors to switch between multiple frequencies and voltage points for better power saving and heat dissipation. CPU turbo ratio can be fixed when Intel SpeedStep Technology set Disabled and Intel Turbo Boost Technology set Enabled.



Please note that enabling this function may reduce CPU voltage and lead to system stability or compatibility issues with some power supplies. Please set this item to [Disabled] if above issues occur.

# Intel Turbo Boost Technology

Intel Turbo Boost Technology enables the processor to run above its base operating frequency when the operating system requests the highest performance state.

#### AVX P1

This allows you to select AVX P1 level.

#### Intel SST-PP

This allows you to select hardware supported level.

## Dynamic SST-PP

This allows you to enable or disable the Dynamic SST-PP.



HWP Native Mode is a pre-requisite for enabling Dynamic SST-PP.

#### Activate SST-BP

This allows you to enable or disable the SST-BP.



HWP Native Mode is a pre-requisite for enabling SST-BF; HWP Native Mode with No Legacy is a pre-requisite for configuring SST-BF.

# Configure SST-BP

This allows you to enable or disable the BIOS to configure SST-BF High Priority Cores so that SW does not have to configure.

#### Hardware P-States

This item supports below selections:

Disable: Hardware chooses a P-state based on OS Request (Legacy P-States).

Native Mode: Hardware chooses a P-state based on OS guidance.

Out of Band Mode: Hardware autonomously chooses a P-state (no OS guidance)

**Native Mode with No Legacy Support:** Hardware autonomously chooses a P-state based on OS guidance with no legacy support.

#### SST-CP

This allows you to enable or disable the SST-CP feature.



About SST configurations are base on the Intel® related supported specifications.

#### **Enable Monitor MWAIT**

This item allows you to configure Monitor and MWAIT instructions whether Auto maps to enable.

## **CPU C6 State Support**

This item allows you to configure the CPU C6 (ACPI C3) report to OS.

#### Enhanced Halt State (C1E)

This item specific the Core C1E auto promotion Control whether takes effect after reboot.

# Package C state Support

This item specific the Package C State limit, the state Auto maps is program specific.

# English

# **CPU Thermal Throttling**

This allows you to enable or disable Thermal Monitor.

## **Power Performance Tuning**

This allows you to decides which controls EFB.

OS Controls EPB: Specifies IA32\_ENERGY\_PERF\_BIAS is used.

BIOS Controls EPB: Specifies ENERGY\_PERF\_BIAS\_CONFIG is used.

PECI Controls EPB: Specifies PCS53 is used.

### ENERGY\_PERF\_BIAS\_CFG mode

This allows you to use input from ENERGY\_PERF\_BIAS\_CONFIG mode seletion. PERF/Balanced, Perf/Bananced or Power/Power.

## ENERGY PERF BIAS CFG mode

This allows you to use input from ENERGY\_PERF\_BIAS\_CONFIG mode seletion. PERF/Balanced, Perf/Bananced or Power/Power.

### Long Duration Power Limit

This allows you to configure the Long Duration Power Limit. PL1 Power Limit is in Watts and the value may vary from 0 to Fused Value. If the value is 0, the fused value will be programmed. A value greater than fused TDP value will not be programmed.

## Long Duration Maintained

This allows you to configure the Long Duration Maintained value. PL1 value is in seconds. The value may vary from 0 to 448. Indicates the time window over which TDP value should be maintained.

#### Short Duration Power Limit

This allows you to configure the Short Duration Power Limit. PL2 Power Limit in Watts. The value may vary from 0 to Fused Value. If the value is 0, BIOS programs 120% \* TDP.

#### Short Duration Maintained

This allows you to configure the Short Duration Maintained value. PL2 value is in seconds. The value may vary from 0 to 0.438. Indicates the time window over which TDP value should be maintained.

# 3.3.3 DRAM Configuration



# **Enforce DDR Memory Frequency POR**

Enable to enforce POR restrictions for DDR frequency and voltage programming.

# **DRAM Frequency**

If [Auto] is selected, the motherboard will detect the memory module(s) inserted and assign the appropriate frequency automatically.

# Volatile Memory Mode

Use this item to select 1LM or 2LM mode for volatile memoy. For 2LM memory mode, BIOS will try to configure 2LM but if BIOS is unable to configure 2LM, volatile memory mode will fall back to 1LM.

# DIMM Interleaving

Enable this item to allow interleaving to be performed at the highest possible MC \* Channel interleaving ways or disable it to enforce interleaving to only 1-way.

# 3-Way Channel Interleaving

Enable this item to allow 3-way channel interleaving or disable it to default to number of channels available per MC when DIMM interleaving is enabled.

# Max Rank Interleaving in IMC

This allows you to configure the rank interleaving settings.

# Englis

#### Mirror Mode

Mirror Mode will set entire 1LM/2LM memory in system to be mirrored, consequently reducing the memory capacity by half. Mirror Enable will disable XPT Prefetch.

## Partial Mirror 1 Size (GB)

Configure the Mirror Mode to Partial Mirror Mode to appear this item. This allows you to select multiplier of 1GB for the size of SAD to be created.

# **ADDDC Sparing**

Enable or disable Memory Rank Sparing.

#### Patrol Scrub

Patrol Scrub is a background activity initiated by the processor to seek out and fix memory errors. The default value is [Enabled].

# Data Scrambling for DDR4/5

This allows you to enable data scrambling for DDR4/DDR5 or disable this feature.

## Data Scrambling for PMem

This allows you to enable data scrambling for PMem or disable this feature. It also allows you to set this feature depending on stepping.

# 3.3.4 Chipset Configuration



#### MMCFG Base

Use this item to select MMCFG Base.

#### MMCFG Size

Use this item to select MMCFG Base.

## MMIO High Base

Use this item to select MMIO High Base.

## MMIO High Granularity Size

Use this item to select MMIO Granularity Size.

#### Onboard VGA

Use this to enable or disable the Onboard VGA function.

#### Onboard LAN

Use this to enable or disable the Onboard LAN function.

#### VT-d

Intel Virtualization Technology for Directed I/O helps your virtual machine monitor better utilize hardware by improving application compatibility and reliability, and providing additional levels of manageability, security, isolation, and I/O performance.

# **SR-IOV Support**

If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support.

#### PCIF Link Width

Select this item to configue PCIE Link Width.

#### PCIE4/5/6/7 Link Width

This allows you to select PCIE4/5/6/7 Link Width. The default value is [Auto].

#### MCIO1/2 Link Width

This allows you to select MCIO1/2 Link Width. The default value is [Auto].

## PCIE Link Speed

Select this item to configure PCIE Link Speed.

## MSAS1/2 Link Speed

This allows you to select MSAS1/2 Link Speed. The default value is [Auto].

## PCIE4/5/6/7 Link Speed

This allows you to select PCIE4/5/6/7 Link Speed. The default value is [Auto].

## MCIO1-1/1-2/ 2-1/2-2 Link Speed

This allows you to select MCIO Link Speed. The default value is [Auto].

## PCIE Hot Plug

Select this item to configure PCIE Hot Plug globally.

## MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2 Hot Plug

This allows you to enable or disable MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2 Hot Plug.

## MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2 Surprise Hot Plug

This allows you to enable or disable MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2 Surprise Hot Plug.

#### PCIE ASPM

Selec this item to configure the PCIE ASPM.

## PCI-E ASPM Support (Global)

Select this item to disables the ASPM support to all PCIe root ports.

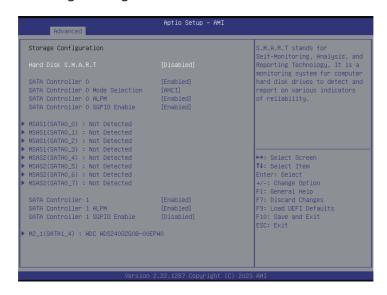
# MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2 ASPM Support

This allows you to configure the ASPM support to MSAS1/2, PCIE4/5/6/7, MCIO1-1/1-2/2-1/2-2.

# Onboard Debug Port LED

Enable or disable the onboard Dr. Debug LED.

# 3.3.5 Storage Configuration



#### Hard Disk S.M.A.R.T.

S.M.A.R.T stands for Self-Monitoring, Analysis, and Reporting Technology. It is a monitoring system for computer hard disk drives to detect and report on various indicators of reliability.

#### SATA Controller 0

Use this item to enable or disable SATA controller.

#### SATA Controller 0 Mode Selection

Identify the SATA port is connected to Solid State Drive or Hard Disk Drive. Press <Ctrl+I> to enter RAID ROM during UEFI POST process.

### SATA Controller 0 ALPM

Use this item to enable or disable Aggressive Link Power Management.

#### SATA Controller 0 SGPIO Enable

Use this item to enable or disable Serial GPIO for SATA controller.

#### MSAS1 (SATA0 0/1/2/3/4/5/6/7)

Select this item to configure the External SATA, Hot Plug, Spin Up Device and SATA Device Type.

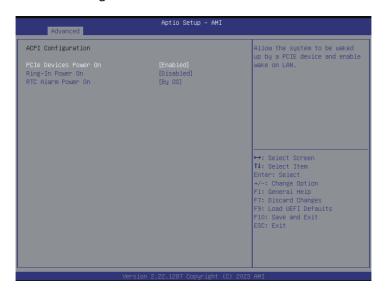
# 3.3.6 NVMe Configuration



## Launch NVMe driver

Select this item to enable or disable launch NVMe driver.

## 3.3.7 ACPI Configuration



#### PCle Devices Power On

Allow the system to be waked up by a PCIE device and enable wake on LAN.

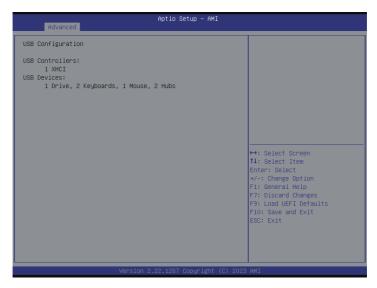
### Ring-In Power On

Use this item to enable or disable Ring-In signals to turn on the system from the power-soft-off mode.

#### RTC Alarm Power On

Use this item to enable or disable RTC (Real Time Clock) to power on the system.

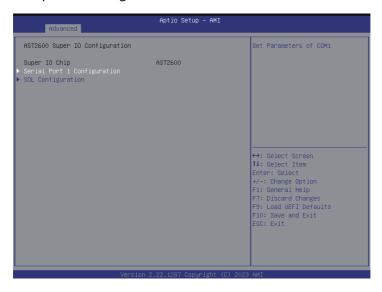
## 3.3.8 USB Configuration



## **USB** Configuration

The USB Configuration displays the USB Controllers and USB Device informations.

## 3.3.9 Super IO Configuration



## Serial Port 1 Configuration

Use this item to set parameters of Serial Port 1 (COM1).

#### Serial Port

Use this item to enable or disable the serial port.

#### Change Settings

Use this item to select an optimal setting for Super IO device.

## **SOL** Configuration

Use this item to set parameters of SOL.

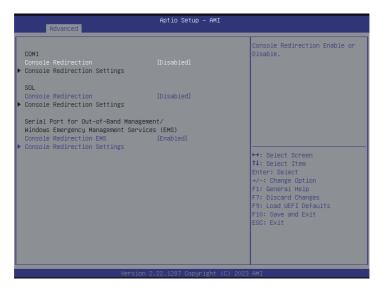
#### SOL Port

Use this item to set parameters of SOL.

## **Change Settings**

Use this item to select an optimal setting for Super IO device.

#### 3.3.10 Serial Port Console Redirection



#### COM1/SOL

#### Console Redirection

Use this option to enable or disable Console Redirection. If this item is set to Enabled, you can select a COM Port to be used for Console Redirection.

## Console Redirection Settings

Use this option to configure Console Redirection Settings, and specify how your computer and the host computer to which you are connected exchange information. Both computers should have the same or compatible settings.

#### **Terminal Type**

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100+	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

#### Bits Per Second

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [38400], [57600] and [115200].

#### **Data Bits**

Use this item to set the data transmission size. The options include [7] and [8] (Bits).

#### Parity

Use this item to select the parity bit. The options include [None], [Even], [Odd], [Mark] and [Space].

#### **Stop Bits**

The item indicates the end of a serial data packet. The standard setting is [1] Stop Bit. Select [2] Stop Bits for slower devices.

#### Flow Control

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None] and [Hardware RTS/CTS].

#### **VT-UTF8 Combo Key Support**

Use this item to enable or disable the VT-UTF8 Combo Key Support for ANSI/VT100 terminals.

#### Recorder Mode

Use this item to enable or disable Recorder Mode to capture terminal data and send it as text messages.

#### Resolution 100x31

Use this item to enable or disable extended terminal resolution support.

#### **Putty Keypad**

Use this item to select Function Key and Keypad on Putty.

#### Legacy Console Redirection

## Legacy Console Redirection Settings

Use this option to configure Legacy Console Redirection Settings, and specify how your computer and the host computer to which you are connected exchange information.

#### **Redirection COM Port**

Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.

#### Resolution

On Legacy OS, the Number of Rows and Columns supported redirection.

#### Redirect After POST

When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

## Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

#### Console Redirection

Use this option to enable or disable Console Redirection. If this item is set to Enabled, you can select a COM Port to be used for Console Redirection.

#### **Console Redirection Settings**

Use this option to configure Console Redirection Settings, and specify how your computer and the host computer to which you are connected exchange information.

#### **Out-of-Band Mgmt Port**

Microsof t Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.

#### **Terminal Type EMS**

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100+	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

#### **Bits Per Second EMS**

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [57600] and [115200].

#### Flow Control EMS

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None], [Hardware RTS/CTS], and [Software Xon/Xoff].

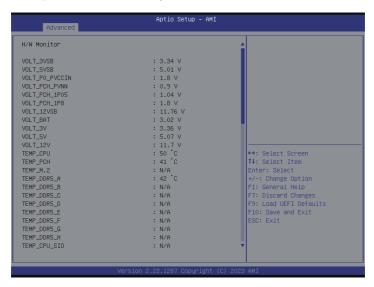
**Data Bits EMS** 

**Parity EMS** 

**Stop Bits EMS** 

#### 3.3.11 H/W Monitor

In this section, it allows you to monitor the status of the hardware on your system, including the parameters of the CPU temperature, motherboard temperature, CPU fan speed, chassis fan speed, and the critical voltage.



## Watch Dog Timer

This allows you to enable or disable the Watch Dog Timer. The default value is [Auto].

## 3.3.12 Runtime Error Logging



#### System Error

Use this item to enable or disable System Error feature. When it is set to [Enabled], you can configure Memory Error and PCIE Error log features.

## WHEA Support

Use this item to enable or disable Windows Hardware Error Architecture.

## **EMCA Logging Support**

Use this item to enable or disable EMCA Logging.

## IIO/PCH Global Error Support

Use this item to enable or disable IIO/PCH Error Support.

## Memory Correctable Error

Use this item to enable or disable Memory Correctable Error.

## Memory Correctable Error Threshold

Correctable Error Threshold (0 - 0x7FFF) used for sparing, tagging, and leaky bucket.

#### PCIE Corrected Error Enable

Use this item to enable or disable PCIe Correctable errors.

# English

## PCIE Corrected Error Threshold

PCIE Correctable Error Threshold (0x01-0xFF) used for sparing, tagging, and leaky bucket.

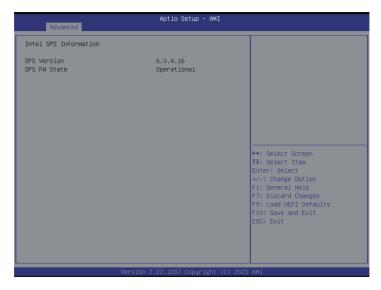
#### PCIE Uncorrected Error Enable

Use this item to enable or disable PCIe Uncorrectable errors.

#### PCIE Fatal Error Enable

Use this item to enable or disable PCIe Ftal errors.

## 3.3.13 Intel SPS Information



SPS screen displays the Intel SPS information, such as Operational Firmware Version and Firmware State.

## 3.3.14 Network Stack Configuration



#### Network Stack

Enable UEFI network stack can prevents you from performing single-user network boots and network installation. If disabled, the host does not use the network interface.

## **IPv4 PXE Support**

Enable IPv4 PXE Boot support. If disabled, IPv4 PXE Boot Option is not supported.

## **IPv4 HTTP Support**

Enable IPv4 HTTP Boot support. If disabled, IPv4 HTTP Boot Option is not supported.

## IPv6 PXE Support

Enable IPv6 PXE Boot support. If disabled, IPv6 PXE Boot Option is not supported.

## IPv6 HTTP Support

Enable IPv6 HTTP Boot support. If disabled, IPv6 HTTP Boot Option is not supported.

#### **PXE Boot Wait Time**

Specifies the wait time and press the ESC key to abort the PXE boot.

#### Media Detect Count

Specifies the number of times the presence of physical storage device are verified on a system reset or power cycle.

## 3.3.15 Intel® VMD technology



## VMD Config for PCH ports

Use this item to enter VMD for PCH ports configuration menu.

#### Enable/Disable VMD

Use this item to enable or disable VMD in this Stack.

#### PCH Root Port 0/2 (MSAS1/2)

Use this item to configure PCH root port.

#### Hot Plug Capable

Use this item to enable or disable Hot Plug for PCIe root ports.

#### Intel(R) VMD for Volume Management Device on Socket 0

Use this item to enter Intel(R) Volume Management Device Technology configuration.

## VMD Config for IOU0 (PCIE5)/IOU1 (PCIE6)/IOU2 (PCIE4)/IOU3 (PCIE7)/IOU4 (MCIO2/MCIO1)

Use this item to enable or disable Intel(R) Volume Management Device Technology in specific Stack.

When [Enabled], users are allowed to configure the options below.

# English

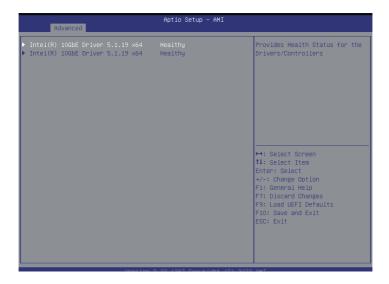
## VMD port X

Use this item to enable or disable Intel(R) Volume Management Device Technology on specific root port.

## Hot Plug Capable

Use this item to enable or disable Hot Plug for specific Ports.

## 3.3.16 Driver Health



Interl (R) 10GbE Driver 5.1.19 x64

This provides Health Status for the drivers and controllers.

## 3.3.17 Tls Auth Configuration



## Server CA Configuration

Press <Enter> to configure Server CA.

## Client Cert Configuration

Press <Enter> to configure Client Cert.

#### **Enroll Cert**

Press <Enter> to enroll cert.

#### Delete Cert

Press <Enter> to delete cert.

#### 3.3.18 Instant Flash

Instant Flash is a UEFI flash utility embedded in Flash ROM. This convenient UEFI update tool allows you to update system UEFI without entering operating systems first like MS-DOS or Windows. Just save the new UEFI file to your USB flash drive, floppy disk or hard drive and launch this tool, then you can update your UEFI only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system. If you execute Instant Flash utility, the utility will show the UEFI files and their respective information. Select the proper UEFI file to update your UEFI, and reboot your system after the UEFI update process is completed.

## 3.4 Server Mgmt



#### Wait For RMC

Wait For BMC response for specified time out. BMC starts at the same time when BIOS starts during AC power ON. It takes around 90 seconds to initialize Host to BMC interfaces.

#### FRB-2 Timer

This allows you to enable or disable FRB-2 timer (POST timer)

#### FRB-2 Timer Timeout

This allows you to define the FRB-2 Time Expiration between 1 to 30 value.

#### FRB-2 Timer Policy

Configure how the system should respond. If the FRB-2 Timer expires is disabled, this item is not available.

## **OS Watchdog Timer**

This allows you to enable or disable OS Watchdog Timer. If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads.

#### OS Wtd Timer Timeout

Configure the OS Boot Watchdog Timer Expiration between 1 to 30 min value. If the OS Boot Watchdog Timer is disabled, this item is not available.

## **OS Wtd Timer Policy**

Configure how the system should respond if the OS Boot Watchdog Timer expires. If the OS Boot Watchdog Timer is disabled, this item is not available.

## **BMC Network Configuration**

This allows you to configure BMC network parameters.

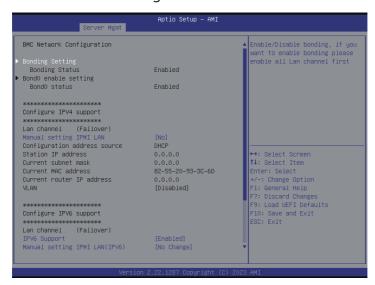
## System Event Log

Press <Enter> to change the SEL event log configuration.

#### **BMC Tools**

This allows you to configure about KCS control, restore AC power loss and load BMC default setings.

## 3.4.1 BMC Network Configuration



#### **Bonding Setting**

Use this item to enable or disable bonding, if you want to enable bonding please enable all Lan channel first.

## **Bond Enable Setting**

Show you the Bond status is enabled or disabled.

#### Lan Channel (Failover)

#### Manual Setting IPMI LAN

If [No] is selected, the IP address is assigned by DHCP. If you prefer using a static IP address, toggle to [Yes], and the changes take effect after the system reboots. The default value is [No].

## Configuration Address Source

Select to configure BMC network parameters statically or dynamically(by BIOS or BMC). Configuration options: [Static] and [DHCP].

**Static**: Manually enter the IP Address, Subnet Mask and Gateway Address in the BIOS for BMC LAN channel configuration.

**DHCP**: IP address, Subnet Mask and Gateway Address are automatically assigned by the network's DHCP server.



When [DHCP] or [Static] is selected, do NOT modify the BMC network settings on the IPMI web page.



The default login information for the IPMI web interface is: Username: admin

Password: admin

For more instructions on how to set up remote control environment and use the IPMI management platform, please refer to the IPMI Configuration User Guide or go to the Support website at: http://www.asrockrack.com/support/ipmi.asp

#### **VLAN**

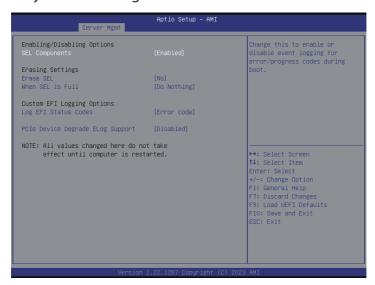
## **IPV6 Support**

Enable or Disable LAN1 IPV6 Support.

## Manual Setting IPMI LAN (IPV6)

Select to configure LAN channel parameters statically or dynamucally (by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

## 3.4.2 System Event Log



#### **SEL Components**

Change this to enable ro disable event logging for error/progress codes during boot.

#### Frase SFI

Use this to choose options for earsing SEL.

#### When SEL is Full

Use this to choose options for reactions to a full SEL.

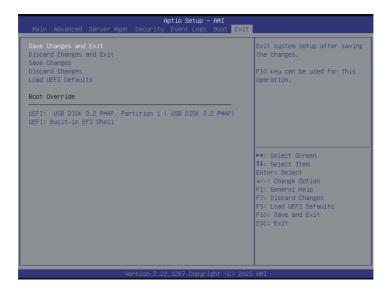
#### Log EFI Status Codes

Use this item to disable the logging of EFI Status Codes or log only error code or only progress code or both.

## PCIe Device Degrade ELog Support

Use this item to enable or disable PCIe Device Degrade Error Logging Support.

#### 3.4.3 BMC Tools



#### KCS control

Select the KSC interface state after POST end. If [Enabled] is selected, the BMC will remain KCS interface after POST stage. If [Disabled] is selected, the BMC will disable KCS interface after POST stage.

#### Restore AC Power Loss

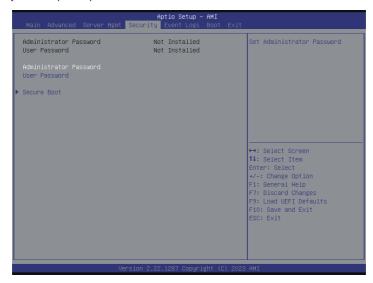
This allows you to set the power state after an unexpected AC/power loss. If [Power Off] is selected, the AC/power remains off when the power recovers. If [Power On] is selected, the AC/power resumes and the system starts to boot up when the power recovers. If [Last State] is selected, it will recover to the state before AC/power loss.

## **Load BMC Default Settings**

Use this item to load BMC default settings.

## 3.5 Security

In this section, you may set or change the supervisor/user password for the system. For the user password, you may also clear it.



#### Administrator Password

Set or change the password for the administrator account. Only the administrator has authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

#### User Password

Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

#### Secure Boot

Use this to enable or disable Secure Boot Control. The default value is [Disabled]. Enable to support Windows Server 2012 R2 or later versions Secure Boot.

#### Secure Boot Mode

Secure Boot mode selector: Standard/Custom. In Custom mode Secure Boot Variables can be configured without authentication.

## 3.5.1 Key Management

In this section, expert users can modify Secure Boot Policy variables without full authentication.



## **Factory Key Provision**

Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.

## Install Default Secure Boot Keys

Please install default secure boot keys if it's the first time you use secure boot.

## Enroll Efi Image

Allow the image to run in Secure Boot mode. Enroll SHA256 hash of the binary into Authorized Signature Database (db).

#### Restore DB defaults

Restore DB variable to factory defaults.

#### Platform Key(PK)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST

- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Default, External, Mixed

#### Key Exchange Keys

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Default, External, Mixed

Key Source: Default, External, Mixed, Test

## **Authorized Signatures**

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Default, External, Mixed

## Forbidden Signatures

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Default, External, Mixed

#### **Authorized TimeStamps**

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

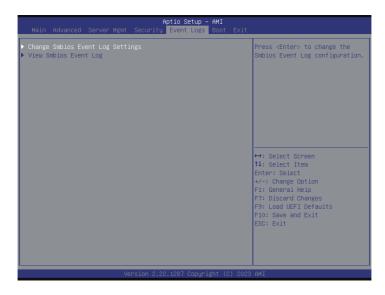
Key Source: Default, External, Mixed

## OsRecovery Signatures

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

## 3.6 Event Logs



### **Change Smbios Event Log Settings**

This allows you to configure the Smbios Event Log Settings.

When entering the item, you will see the followings:

#### **Smbios Event Log**

Use this item to enable or disable all features of the SMBIOS Event Logging during system boot

#### **Erase Event Log**

The options include [No], [Yes, Next reset] and [Yes, Every reset]. If Yes is selected, all logged events will be erased.

#### When Log is Full

Use this item to choose options for reactions to a full Smbios Event Log. The options include [Do Nothing] and [Erase Immediately].

#### Log System Boot Event

Choose option to enable/disable logging of System boot event.

#### **View Smbios Event Log**

Press <Enter> to view the Smbios Event Log records.



All values changed here do not take effect until computer is restarted.

#### 3.7 Boot

In this section, it will display the available devices on your system for you to configure the boot settings and the boot priority.



#### **Boot Option #1**

Use this item to set the system boot order.

#### **Boot Option #2**

Use this item to set the system boot order.

#### **Boot Option #3**

Use this item to set the system boot order.

#### **Boot Option #4**

Use this item to set the system boot order.

#### **Boot Option #5**

Use this item to set the system boot order.

#### **Boot Option #6**

Use this item to set the system boot order.

# English

#### **UEFI USB Drive BBS Priorities**

Specifies the Boot Device Priority sequence from available UEFI USB Drives.

#### **UEFI Application Boot Priorities**

Specifies the Boot Device Priority sequence from available UEFI Application.

## **Setup Prompt Timeout**

Configure the number of seconds to wait for the UEFI setup utility.

#### **Bootup Num-Lock**

If this item is set to [On], it will automatically activate the Numeric Lock function after boot-up.

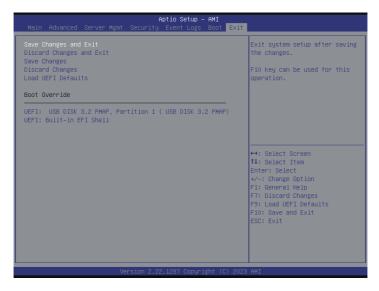
#### **Boot Beep**

Select whether the Boot Beep should be turned on or off when the system boots up. Please note that a buzzer is needed.

#### Full Screen Logo

Use this item to enable or disable OEM Logo. The default value is [Enabled].

#### 3.8 Exit Screen



#### Save Changes and Exit

When you select this option, the following message "Save configuration changes and exit setup?" will pop-out. Press <F10> key or select [Yes] to save the changes and exit the UEFI SETUP UTILITY.

## Discard Changes and Exit

When you select this option, the following message "Discard changes and exit setup?" will pop-out. Press <ESC> key or select [Yes] to exit the UEFI SETUP UTILITY without saving any changes.

## Save Changes

When you select this option, the following message "Save changes?" will pop-out. Press <F7> key or select [Yes] to save all changes.

## **Discard Changes**

When you select this option, the following message "Discard changes?" will pop-out. Press <F7> key or select [Yes] to discard all changes.

#### Load UEFI Defaults

Load UEFI default values for all the setup questions. F9 key can be used for this operation.

## **Chapter 4 Software Support**

After all the hardware has been installed, we suggest you go to our offical website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a> and make sure if there are any new updates of the BIOS / BMC firmware for your motherboard.

## 4.1 Download and Install Operating System

This motherboard supports various Microsoft\* Windows\* Server / Linux compliant operating systems. Please download the operating system from your OS manufacturer. Please refer to your OS documentation for more instructions.

\* Please download the Intel\* SATA Floppy Image driver from the ASRock Rack's website (www.asrockrack.com) to your USB drive while installing OS in SATA RAID mode.

#### 4.2 Download and Install Software Drivers

This motherboard supports various Microsoft\* Windows\* compliant drivers. Please download the required drivers from our website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a>.

To download necessary drivers, go the the product page, click on the "Download" tab, choose the operating system you use, and select the driver you need to be donwloaded.

### 4.3 Contact Information

If you need to contact ASRock Rack or want to know more about ASRock Rack, welcome to visit ASRock Rack's website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a>; or you may contact your dealer for further information.

## **Chapter 5 Troubleshooting**

## 5.1 Troubleshooting Procedures

Follow the procedures below to troubleshoot your system.



Always unplug the power cord before adding, removing or changing any hardware components. Failure to do so may cause physical injuries to you and damages to motherboard components.

- 1. Disconnect the power cable and check whether the PWR LED is off.
- Unplug all cables, connectors and remove all add-on cards from the motherboard. Make sure that the jumpers are set to default settings.
- 3. Confirm that there are no short circuits between the motherboard and the chassis.
- 4. Install a CPU and fan on the motherboard, then connect the chassis speaker and power LED

#### If there is no power...

- 1. Confirm that there are no short circuits between the motherboard and the chassis.
- 2. Make sure that the jumpers are set to default settings.
- 3. Check the settings of the 115V/230V switch on the power supply.
- Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.

#### If there is no video...

- 1. Try replugging the monitor cables and power cord.
- 2. Check for memory errors.

#### If there are memory errors...

- 1. Verify that the DIMM modules are properly seated in the slots.
- 2. Use recommended DDR4 RDIMM/RDIMM-3DS/LRDIMM/LRDIMM-3DS/ Intel®Optane™ Persistent Memory 200 Series.
- If you have installed more than one DIMM modules, they should be identical with the same brand, speed, size and chip-type.
- 4. Try inserting different DIMM modules into different slots to identify faulty ones.
- 5. Check the settings of the 115V/230V switch on the power supply.

## English

#### Unable to save system setup configurations...

- 1. Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.
- 2. Confirm whether your power supply provides adaquate and stable power.

## Other problems...

 Try searching keywords related to your problem on ASRock Rack's FAQ page: http://www.asrockrack.com/support

## 5.2 Technical Support Procedures

If you have tried the troubleshooting procedures mentioned above and the problems are still unsolved, please contact ASRock Rack's technical support with the following information:

- 1. Your contact information
- 2. Model name, BIOS version and problem type.
- 3. System configuration.
- 4. Problem description.

You may contact ASRock Rack's technical support at: http://www.asrockrack.com/support/tsd.asp

## 5.3 Returning Merchandise for Service

For warranty service, the receipt or a copy of your invoice marked with the date of purchase is required. By calling your vendor or going to our RMA website (http://event. asrockrack.com/tsd.asp) you may obtain a Returned Merchandise Authorization (RMA) number

The RMA number should be displayed on the outside of the shipping carton which is mailed prepaid or hand-carried when you return the motherboard to the manufacturer. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty does not cover damages incurred in shipping or from failure due to alteration, misuse, abuse or improper maintenance of products.

Contact your distributor first for any product related problems during the warranty period.

### **Contact Information**

If you need to contact ASRock Rack or want to know more about ASRock Rack, you're welcome to visit ASRock Rack's website at http://www.asrockrack.com; or you may contact your dealer for further information. For technical questions, please submit a support request form at https://event.asrockrack.com/tsd.asp

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